



Indian Institute of Technology Indore
Simrol, Khandwa Road, Indore 453 552
Department of Computer Science and Engineering

IIT Indore

Date: 11th June 2024

Advertisement for Admission to Ph.D. Program in Computer Science and Engineering (CSE) under Visvesvaraya PhD Scheme

IIT Indore invites applications from highly motivated and research-oriented students for admission to its PhD program in the Department of Computer Science and Engineering for the Spring Semester of Academic Year (AY) 2023-24 under **Visvesvaraya PhD scheme of Ministry of Electronics and Information Technology (MeitY)**.

Time Schedule of Interview Process:

Last date of online application through https://academic.iiti.ac.in:8443/nregistration.jsp (for Indian applicants)	7th July, 2024 (Sunday) Latest by 17.00 hrs. IST
Dates of PhD selection process (online written test and/or online interviews)	9th July, 2024 (Tuesday)

Minimum Educational Qualifications (MEQs) and Qualifying Examination

Master's degree (preferably M. Tech) in Computer Science/Electrical/ Electronics/ Electronics & Communication/ Computer Technology with specialization in the areas of **Networking, Network Security, Blockchain, Hardware Security and Cryptography, Distributed Systems**, or any other relevant area with first division as defined by the awarding institute/university preferably with GATE qualification in CS/EE/EC.

OR

Four-year Bachelor's degree OR five-year integrated degree in Computer Science/Electrical Engineering/ Electronics and Communication Engineering OR Electrical & Electronics Engineering (with first division as defined by the awarding Institute/University) AND valid GATE qualification in CS/EE/EC only.

Financial Support for Full-time PhD candidates under Visvesvaraya PhD Scheme:

Fellowship: Rs. 38,750 per month in 1st & 2nd year and Rs. 43,750 per month in 3rd, 4th, and 5th year of PhD (support till PhD completion or 5 years whichever is earlier).

Research Contingency Grant Support: An amount of Rs. 1,20,000 per year for support duration of PhD candidate.



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Instructions:

1. All interested candidates, fulfilling the minimum eligibility criteria, must apply online through the website (<http://academic.iiti.ac.in:8080/nregistration.jsp>).
2. After applying online, the applicants should take a printout of the application form and sign the same. The scanned copy of duly signed application form along with the following documents should be sent by email to admission-cse@iiti.ac.in
 - a. Self-attested photocopies/scanned originals of all relevant supporting documents such as degree certificates, mark sheets from 10th class onwards, date of birth certificate, **GATE score card**, **fellowship award letter**, etc., that they wish to present before the selection committee.
 - b. Two recent passport size photographs.
 - c. Receipt of fee (Rs.100/-) paid through SBI i-collect.
 - d. Letters of recommendation duly signed by a minimum of **two referees**, who have known the applicant in a professional capacity, are mandatory for PhD selection process and must be sent directly to admission-cse@iiti.ac.in
The format of the recommendation letter can be found along with this advertisement or using this link: <https://bit.ly/3Fs161p>
3. **Important: DO NOT** send any form or documents by post.
4. **Online PhD written test and/or online interviews** shall be conducted on **July 9th, 2024**. **Only shortlisted candidates will be called for online written test and/or online interviews, and the same will be shared by email to respective candidates on 8th July 2024, 15.00 hrs. No emails or communication, in any form, regarding shortlisting process, change of interview date, syllabus of the interview, etc. will be entertained.**
5. The areas of interest and detailed profiles of faculty members are given below.
6. Mere fulfillment of the minimum eligibility criteria does not entitle anyone for admission into the PhD program in the Department of Computer Science and Engineering.

Faculty Profile:



Dr. Bodhisatwa Mazumdar is an Associate Professor in the Department of Computer Science and Engineering, and Associate Dean-R&D II, at IIT Indore. He received the M.S. and Ph.D. degrees from the Indian Institute of Technology (IIT) Kharagpur, Kharagpur, India. He was a Postdoctoral Researcher with the Design for Excellence Laboratory, New York University Abu Dhabi, Abu Dhabi, UAE. His current research areas include logic synthesis techniques for security, optimized hardware implementations of cryptographic primitives, and machine learning-based fault-based side channel attacks and countermeasures. He is presently a voting-cum working group member (WGM) in IEEE Standard 1413.1 on Guide for Selecting and Using Reliability Predictions, and IEEE 1624, the IEEE Standard for Organizational and Capability. He has been a Technical Program Committee Member of conferences, such as VLSID, SPACE, and VDAT. Presently, he is seeking applications under Visvesvaraya PhD Scheme, Phase-II under MeitY, Govt. of India in the above-mentioned research areas. For details regarding his

publications, refer to

https://scholar.google.co.in/citations?hl=en&user=ea-e9TwAAAAJ&view_op=list_works&sortby=pubdate



Dr. Subhra Mazumdar is an Assistant Professor in the Department of Computer Science and Engineering (CSE), Indian Institute of Technology Indore. Before that, she was a post-doctoral researcher in the Security and Privacy (S&P) Research Unit at Vienna University of Technology, Austria, where she worked on rational attacks on Layer 2 protocols in Blockchain. She did both Ph.D. and M.tech in Computer Science from Indian Statistical Institute, Kolkata, in 2022 and 2018, respectively. She works in Blockchain, Applied Cryptography, Cloud Computing, and Distributed Systems. She has published papers in reputed venues, including IEEE TDSC, IEEE TNSM, IEEE TETC, ACM CCS, and IEEE TrustCom. Currently, she is looking for a PhD candidate under Visvesvaraya PhD scheme, Phase-II under MeitY, Govt. of India in Blockchain and Distributed Systems, with basic knowledge of cryptography and good programming knowledge in Python/Golang/C++. Interested candidates are encouraged to apply. For details regarding her publications, refer to [Google Scholar Profile](#)



Dr. Sidharth Sharma (<http://people.iiti.ac.in/~sidharth/>) is an Assistant Professor in the Department of Computer Science and Engineering at IIT Indore. He received his Ph.D. degree from the Indian Institute of Technology (IIT) Bombay. His current research areas include AI/ML techniques for network management (including LLMs and Digital Twins), 6G, and network verification. He holds two US patents and his research is funded by several government and private agencies. He has published his research in reputed venues, including IEEE/ACM ToN, IEEE TNSE, IEEE/Optica JLT, ACM TOIT, ICC, ICCCN, and DRCN. Presently, he is seeking applications under the Visvesvaraya PhD Scheme, Phase-II under MeitY, Govt. of India in the above-mentioned research areas. For details regarding his publications, refer to <https://scholar.google.com/citations?user=c0R1WokAAAAJ&hl=en>