

INDIAN INSTITUTE OF TECHNOLOGY INDORE

Advertisement for Admission to Ph.D. Program Visvesvaraya PhD Scheme for Electronics & IT (MeiTY) Autumn Semester, Academic Year 2026-27

Ref: IITI/Acad/Visvesvaraya PhD Admissions/25-26/R3

Date: January 13, 2026

IIT Indore invites applications from highly motivated and research-oriented students for admission to its Ph.D. program through the **Visvesvaraya PhD Scheme for Electronics & IT (MeiTY)** in the domains of **Electronics System Design and Manufacturing (ESDM)** and **Information Technology/IT Enabled Services (IT/ITES)** for Autumn Semester AY 2026-27.

Candidates are advised to carefully review Annexure A for the tentative list of project areas and the corresponding departments before applying. Please note that no separate email will be sent to candidates for appearing in the written test/interview.

Candidates who fulfill the minimum eligibility criteria mentioned below are welcome to attend the test/interview process. The reporting venue is the Security Desk, Lecture Hall Complex, IIT Indore Campus, and the reporting time is 10:00 AM.

IMPORTANT DATES

Event	Date & Time
Last Date for Online Application https://academic.iiti.ac.in:8443/nregistration.jsp (Select FA MeiTY)	January 25, 2026 (Sunday) by 23:59 hrs IST
Deadline for Letters of Recommendation To be sent by referees to: convener-meityschemes@iiti.ac.in	January 25, 2026 (Sunday)
Admission Test and Interview (In-person at IIT Indore)	January 27, 2026 (Tuesday)

MINIMUM EDUCATIONAL QUALIFICATIONS

Educational Qualifications (Any One):

- Master's degree (M.Tech./M.E./M.S.) in CSE, IT, ECE, EEE, Instrumentation and Control Engineering, Mathematics & Computing, or related discipline
- M.Sc. or MS degree in Mathematics/Physics
- Bachelor's degree (BE/BTech) in CSE, IT, ECE, EEE, Instrumentation and Control Engineering, Mathematics & Computing, or related discipline

*All degrees must be with first division**

First Division Criteria:

- Minimum 60% marks (55% for SC/ST/PwD)
- **OR** First class as per university
- **OR** CGPA 6.0/10 (5.5 for SC/ST/PwD)
- **OR** Equivalent CGPA on other scales

Additional Requirements for BE/BTech/M.Sc. Candidates:

Valid GATE score (CS, EC, DA, or EE) **OR** BTech from IIT with CGPA ≥ 8.5

Note: Admission test may include questions from CS, EC, DA, and EE GATE papers. Select admission category as **EA** while applying

FELLOWSHIP & BENEFITS

Ph.D. Fellowship (As per Funding Agency Norms)

₹38,750

per month
(Years 1-2)

₹43,750

per month
(Years 3-5)

₹1,20,000

Research Contingency
per year

+ HRA (if applicable)

APPLICATION PROCEDURE

1. Review **Annexure A** for project areas and departments.
2. Apply online at: <https://academic.iiti.ac.in:8443/nregistration.jsp> (Select FA MeITY)
3. Email signed application form with supporting documents to: convener-meityschemes@iiti.ac.in
4. Two referees must send **Letters of Recommendation** to the same email with subject line:
"LoR for Admission to Ph.D. Program under Visvesvaraya PhD Scheme for Electronics & IT (MeITY) in Autumn Semester AY 2026-2027"
5. Attend in-person admission test/Interview on the **above mentioned date** at IIT Indore, Simrol, Madhya Pradesh-453552

REQUIRED DOCUMENTS (in order)

- Fee payment receipt
- 10th marksheet
- 12th marksheet
- BTech marksheets & certificate
- Master's marksheets & certificate
- Caste certificate (if applicable)
- GATE scorecard (if qualified)
- All other relevant certificates

Send PDF of application with scanned, self-attested copies of documents to convener-meityschemes@iiti.ac.in

IMPORTANT INSTRUCTIONS:

- **In-person attendance mandatory** at IIT Indore, Simrol, MP-453552. No online interviews.
- **No separate email communication will be sent. Eligible candidates must attend the admission process as per the schedule.**
- No requests for date changes or online interviews will be entertained.
- **DO NOT** send hard copies by post. Email submissions only.
- Only one application per candidate permitted.
- Meeting minimum eligibility does not guarantee admission.

FOR QUERIES CONTACT:

Convener, MeitY Schemes

Indian Institute of Technology Indore, Indore - 453552, Madhya Pradesh, India

Email: convener-meityschemes@iiti.ac.in

Annexure A: Proposed Project Areas for PhD Candidates

S. No.	Title of the Project	Name of the Concerned Professor	Department	About the Project
1	Medical Ultrasound Imaging	Dr. Lokesh Basavarajappa	Biosciences and Biomedical Engineering	The research aims to develop an automated system for adjusting ultrasound machine presets for individual patients, addressing challenges faced by novice operators and advancing operator-independent robotic ultrasound systems.
2	Organic Electronics	Dr. Rajneesh Misra	Chemistry, CAE	Design of HTM and ETM for Perovskite solar cells and OLEDs, along with fabrication for improved power conversion efficiency.
3	AgriLLMs for Agricultural Applications	Dr. Aruna Tiwari	Computer Science and Engineering	The project focuses on designing domain-specific Large Language Models (AgriLLMs) to address limitations of general-purpose LLMs in agriculture. It involves curating datasets, integrating knowledge graphs, mitigating hallucination and bias, and optimizing models for tasks like precision advisory, knowledge synthesis, and resource optimization.
4	Autonomous Cyber Physical Systems in Connected Autonomous Vehicles	Dr. Gourinath Banda	Computer Science and Engineering	The research addresses synthetic dataset creation for object/event detection and response in autonomous vehicles and investigates cybersecurity vulnerabilities in communication, sensing, and decision layers. The aim is to advance safety, resilience, and trustworthiness of autonomous mobility systems.

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5	Advancing Brain-Computer Interfaces: Multilingual EEG-to-Text Modeling	Dr. Chandresh Kumar Maurya	Computer Science and Engineering	This research develops an open-vocabulary EEG-to-text dataset and model to decode brain signals into coherent language. It tackles challenges of limited vocabulary and signal noise in BCI by advancing neural signal processing, enabling flexible and accurate brain-to-language translation for multilingual, low-resource communication contexts.
6	Cyber Security in Programmable Dataplanes	Dr. Neminath Hubballi	Computer Science and Engineering	The research aims to detect and mitigate network anomalies in programmable dataplanes (e.g., P4-based switches) by developing efficient, real-time algorithms for anomaly detection at the dataplane level without introducing latency from centralized control.
7	Edge-Cloud Continuum for IoT	Dr. Ayan Mondal	Computer Science and Engineering	The PhD student will work on problems related to task offloading and dynamic resource orchestration in the Edge-Cloud continuum for IoT. The work will also explore the possibility of using Non-Terrestrial Networks (NTN) in IoT environments.
8	Deep Learning Approaches for Overcoming Data and Reconstruction Challenges in Medical Imaging	Dr. Surya Prakash	Computer Science and Engineering	The project develops deep learning frameworks using CNNs, Transformers, GANs, and diffusion models for super-resolution, reconstruction, and synthetic data generation in medical imaging. It leverages self-supervised and semi-supervised learning to reduce annotation costs and harmonize heterogeneous data, enabling clinically reliable and scalable imaging solutions.

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9	EdgeAI and Ubiquitous Systems	Dr. Abhishek Srivastava	Computer Science and Engineering	The research focuses on miniaturizing massive AI models for deployment on constrained architectures such as IoT edge devices. It will explore multiple use cases leveraging edge intelligence for real-world applications.
10	Hardware Security in Machine Learning Accelerators	Dr. Anirban Sengupta	Computer Science and Engineering	The research addresses Trojan attacks on Machine Learning Accelerators, focusing on securing hardware against malicious modifications that can compromise AI performance and safety.
11	Explainable AI for Early Disease Detection Using Imaging, Genomics, and Clinical Data	Dr. M. Tanveer	Mathematics	Leveraging interpretable AI models for healthcare diagnostics by integrating multimodal data sources such as imaging, genomics, and clinical records. The focus is on developing robust algorithms that ensure fairness, transparency, and explainability while achieving high accuracy, with applications in early disease detection.
12	AI-Driven Performance Optimization of PASS with NOMA and ISAC in Industrial IoT Environments	Dr. Vimal Bhatia	Electrical Engineering	The student will research performance bounds for PASS with NOMA and ISAC in IoT communications under challenging factory environments. AI/ML algorithms will be used to achieve optimal system performance for sustainable communications and sensing.
13	Simultaneous Wireless Information and Power Transfer Using Rectenna-Based Systems	Dr. Rinkee Chopra	Electrical Engineering	The research explores simultaneous wireless information and power transfer (SWIPT). By integrating rectennas (antenna + rectifier), the project aims to harvest ambient RF energy and convert it into DC power while enabling communication functions.

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14	Nanoelectronics; Electron Devices and Systems	Dr. Shaibal Mukherjee	Electrical Engineering	(a) Atomic-scale semiconductor and heterostructure-based RRAMs and crossbars for logic circuits, image processing, and neuromorphic computation. (b) Electrochemical sensors for environmental sustainability, healthcare, and agriculture. (c) FETs for sensors. (d) Photodetectors.
15	Differential Game-Theoretic Defense Strategies Against Adversarial Drone Attacks	Dr. Sharad Kumar Singh	Electrical Engineering	The project solves the problem of avoiding an adversarial attack by a drone with the help of defending drones to protect an infrastructure, which is the target of the adversarial attack. We use a differential game theoretic approach to solve the problem, which provides the optimal strategy for each agent involved in the game.
16	Power Electronics Integrated Battery (Pe-Battery) System for Electric Vehicles	Dr. B. Prathap Reddy	Electrical Engineering	The research focuses on active paralleling of battery strings, integrated high-power on-board charging, and bidirectional V2X functionality. It addresses challenges in battery safety, charge equalization, and cost-effective integration of charging functions, aiming to enhance performance and scalability of EV battery systems.
17	VLSI Circuit and System Design	Dr. Santosh Kumar Vishvakarma	Electrical Engineering	The PhD student will work on energy-efficient edge AI hardware accelerators for computer vision. The project includes design, fabrication, and testing of the chip.
18	Advanced Computer Vision for Medical Diagnostics	Dr. Vivek Kanhangad	Electrical Engineering	The research develops advanced computer vision algorithms using deep learning to enhance accuracy and efficiency of automated medical diagnostics. It addresses challenges such as limited annotated data, variability in

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				disease presentation, and real-time prediction requirements.
19	FPGA-Based High-Performance Computing and Signal Processing Solutions	Dr. Srivathsan Vasudevan	Electrical Engineering	The broad area of research is to provide solutions to high-performance computation and signal processing through electronic boards like FPGA. Ultimately, it will be electronic/instrumentation development for specific applications.
20	Energy-efficient cooling strategies for Compact Electronics Components	Dr. S. Dhinakaran	Mechanical Engineering	The project involves CFD simulations to study heat transfer and fluid flow in microfluidic cooling devices for electronic systems. The work will focus on modeling temperature rise, flow distribution and cooling efficiency in microchannel networks. Using multi-physics simulations, the student will develop predictive models that can guide the design of reliable and energy-efficient cooling strategies for compact electronic and IT hardware.
21	Sensors for Structural Health Monitoring	Dr. Shailesh I. Kundalwal	Mechanical Engineering	This project addresses the limitations in understanding the piezoresistive behavior of carbon nanotube yarns (CNTYs), particularly under compressive loads relevant to civil, aerospace, and marine engineering. It aims to establish a facility to comprehensively investigate CNTY-based sensors under compression. Outcomes will contribute to next-generation SHM systems and the broader application of nanotechnology in engineering.

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22	Soft Magnetic Composites (SMCs) for EV and HEV Motors	Dr. Vinod Kumar	MEMS (Materials Science and Engineering)	The research involves design and indigenous development of advanced high-performing soft magnetic composites (SMCs) for electric and hybrid vehicle motors, enhancing efficiency and performance.
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